ATTY DOCKET NO. SERIAL NO. CHA920030019US1 INFORMATION DISCLOSURE CITATION Applicants: Duncan et al. (Use several sheets if necessary) GROUP FILING **U.S. PATENT DOCUMENTS** *EXAMINER FILING DATE DOCUMENT NUMBER DATE NAME CLASS SUBCLASS INITIAL IF APPROPRIATE US 2002/0191599 A1 12/2002 Parthasarathy et al. 42 US 2002/0170951 A1 11/2002 Oshins et al. 11/2002 US 6,487,628 B1 Duong et al. US 6,175,889 B1 1/2001 Olarig 6,134,622 10/2000 Kapur et al. 11/1999 5,991,824 Strand et al. A5c

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
	 DOCOMENT ROMBER	- CATE	COUNTY.			YES	NO
AZ	WO 02/35367 A1	5/2002	PCT		,		
AF	JP2003018188A	1/2003	JP		_		
ASI	JP2000010912A	1/2000	JP .				
			·				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	(1110) 2010)	
ASC	"INFINIBAND: An End-User Perspective," Prepared by The Kent Row Group, date and source unknown, pp. 1-18.	•••••
ASC	Leddy, W., "InfiniBand Software: Design Matters," Lane 15 Software, Inc., v 1.0, June 2001, pp. 1-9.	
EXAMINER	DATE CONSIDERED 1/29106	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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INFO	RMATION DISCLOSURE CITATION	Applicant(s)				
1140	(Use several sheets if necessary)	Duncan et al.				
		Filing Date	Group Art Unit			
*EXAMINER	OTHER DOCUMENTS (Including Author,					
INITIAL	Paavola, S., "Embedded InfiniBand," A SKY Co	omputers White Paper, www.skycon	nputers.com, date unknown.			
ASC						
	Paavola, S., "InfiniBand Architecture Delivers E www.skycomputers.com, date unknown.	Enhanced Reliability," A SKY Comp	puters White Paper,			
/	www.skycomputers.com, date disknown.					
	Tanabe et al., "A Low Latency High Bandwidth Network Interface Prototype for PC Cluster," Int'l Workshop on Innovative Architecture for Future Generation High-Performance Processors and Systems, 10-11 Jan. 2002.					
\ 	Tonahe et al. "A prototype of high bandwidth l	ow latency network interface plugge	ed into a DIMM slot," date and source			
	Tanabe et al., "A prototype of high bandwidth low latency network interface plugged into a DIMM slot," date and source unknown.					
	Duncan et al., "High Performance I/O Design in	the AlphaServer 4100 Symmetric N	Multiprocessing System," Digital Tech.			
	Journal, Vol. 8 No. 4, updated 25 April 1997, pp	s. 1-20.				
I = I						
	Bouvier, D., "RapidIO: The Interconnect Archi	tecture for High Performance Embe	edded Systems," www.rapidio.org, Jan.			
	2003, pp. 1-20.					
1.	•	•				
	Leroux, P., "Making the Switch to RapidIO: Us RapidIO Interconnect," QNX Software Systems	sing a Message-passing Microkernel s Ltd., 2002, pp. 1-11.	OS to Realize the Full Potential of the			
	in The Lightning Data Transport I/O Bus Archit	tecture." LDT Architecture White P	aper Rev. 1001, Api NetWorks, Inc., 2000,			
	pp. 1-18.	"The Lightning Data Transport I/O Bus Architecture," LDT Architecture White Paper Rev. 1001, Api NetWorks, Inc., 2000, pp. 1-18.				
	Lindenstruth, V., "Novel Intelligent I/O Archite	ecture Eliminating the Bus Bottlene	ck," date and source unknown.			
	Endenstratu, v., 110ver intenigent 20 rotemestation and					
	"Realizing the Full Potential of Server, Switch & I/O Blades with InfiniBand Architecture," White Paper, Mellanox					
	Technologies, Inc., Dec. 2001, pp. 1-11.					
	Stigliani, Jr., et al., "IBM eServer z900 I/O Subsystem," IBM Journal of Research and Development, Vol. 46 No. 4/5, July/Sept. 2002, pp. 421-445.					
\ \						
	"Technology Background," Intelligent-IO.com, author and date unknown.					
ASC	,	,				
EXAMINER 2	Mul III	DATE CONSIDERED	124106			

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

•				Docket Number (Optional) CHA92003001		ation Number
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)			Applicant(s) Duncan et al.			
			Filing Date	Group	Art Unit	
*EXAMINER		OTHER DOC	UMENTS (Including Aut	hor, Tisle, Date, Pertinens Pages, El	(c.)	
		Rosdahl, J., "The 12O Host Environment," Novell, Inc., date and source unknown.				
A5C A5C		Easley, M., "Process	sor Independent 120 Sol	utions Using Shell and Private	Platform," www.pl	xtech.com.
11,00	ļ					
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···						
						
EXAMINER		1/11/19	1 (n	DATE CONSIDERED	1/24/0	6

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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and

not considered. Include copy of this form with next communication to applicant.